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501.36127CC3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

KOUBUCHI et al

Serial No.:

10/619,039

Filed:

July 14, 2003

For:

Semiconductor Integrated Circuit Device

Group:

2814

Examiner:

L. Pham

<u>INFORMATION DISCLOSURE STATEMENT</u> <u>UNDER 37 CFR 1.97 & 1.98</u>

Mail Stop: DD

Commissioner For Patents

May 13, 2004

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In the matter of the above-identified application, applicants are submitting herewith copies of the document listed in the attached form equivalent to Form PTO-1449 for the Examiner's consideration.

This information disclosure statement is being submitted before the mailing date of either a final action or a notice of allowance and is accompanied by the fee of \$180.00 set forth in 37 CFR 1.17(p).

The documents listed on the attached form equivalent to Form PTO-1449 is in the English language.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus, LLP, Deposit Account No. 01-2135 (Case: 501.36127CC3), and please credit any excess fees to such deposit account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

Alan E. Schiavelli

Registration No. 32,087

AES/jla (703) 312-6600 Attachments

10/619,039

FORM PTO-1449 (Rev. 4/92)

U.S. Department of Commerce Patent and Trademark Office

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use several sheets if necessary)

501.36127CC3 APPLICANT

KOUBUCHI et al

FILING DATE

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EXAMINER: Initial if citation is considered, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.